

LSI LOGIC

FAX

FEB 02 2006

LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035Date February 2, 2006Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2123

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From: Connie del Castillo

Intellectual Property Paralegal

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REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/706,127
Filing date: November 12, 2003
First named inventor: Maurizio Spadari
Attorney docket number: 03-1868 / 1496.00348

Transmitted herewith for filing via facsimile:

- Status Request Letter

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2/02/06

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Signature

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FEB 02 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Maurizio Spadari

Serial No. :

10/706,127

Group Art Unit : 2123

Filed :

November 12, 2003

Examiner :

For :

Methodology for debugging RTL simulations of processor based system on chip

Atty Docket :

1496.00348 / 03-1868

STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

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